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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Masahito ISODA

Art Unit: 2816

Application No.: 09/811,456

Examiner: L. Nguyen

Filed: March 20, 2001

Docket No.: 108075-00056

For: INPUT BUFFER CIRCUIT FOR SEMICONDUCTOR DEVICE

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TECHNOLOGY CENTER

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

September 3, 2002

Sir:

Please amend the application as follows:

IN THE CLAIMS:

Please amend claims 19 and 38 as follows:

19. (Once Amended) An input buffer circuit comprising:
- a differential amplifier circuit, disposed between a first power supply and a second power supply, for receiving first and second input signals and generating an amplified signal corresponding to a voltage difference between the first and second input signals;
 - a first circuit, coupled to the differential amplifier circuit, for receiving the amplified signal from the differential amplifier circuit;

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